Université Paris-Saclay

Share

Email destinataire :

Vous devez renseigner un email valide

Envoyer

Une erreur est survenue. Merci de réessayer ultérieurement

Le mail de partage a bien été envoyé.

Add to Compare

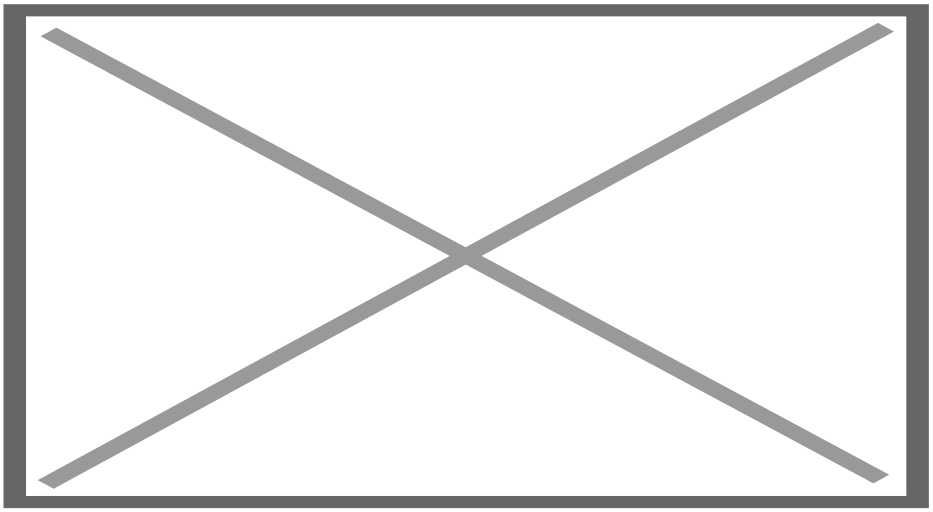
BUT2 Electrical Engineering and Industrial Computing: Electronics and Embedded Systems (ESE)

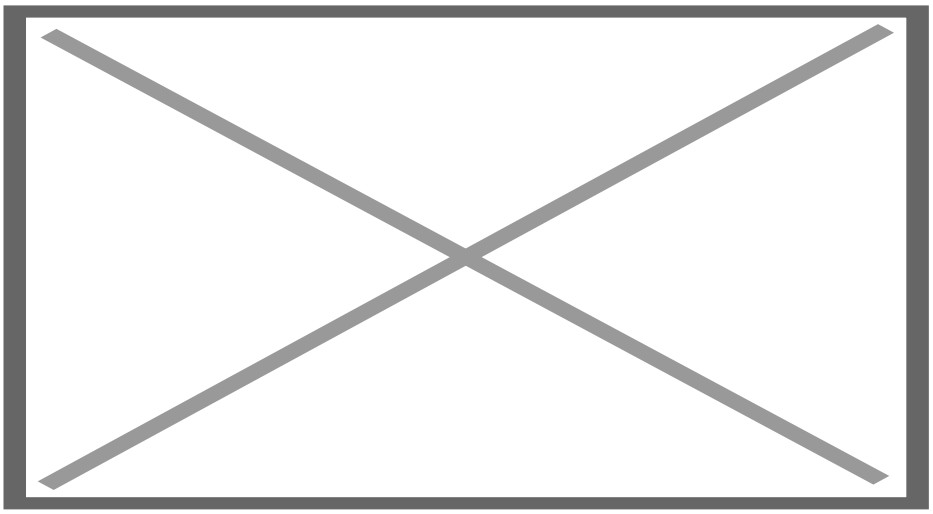
BUT (University technology degree)
Specialisation Génie électrique et informatique industrielle
Enrolment type
Formation initiale
Formation en apprentissage
Formation continue
Language(s) of instruction
German
English
Spanish
French

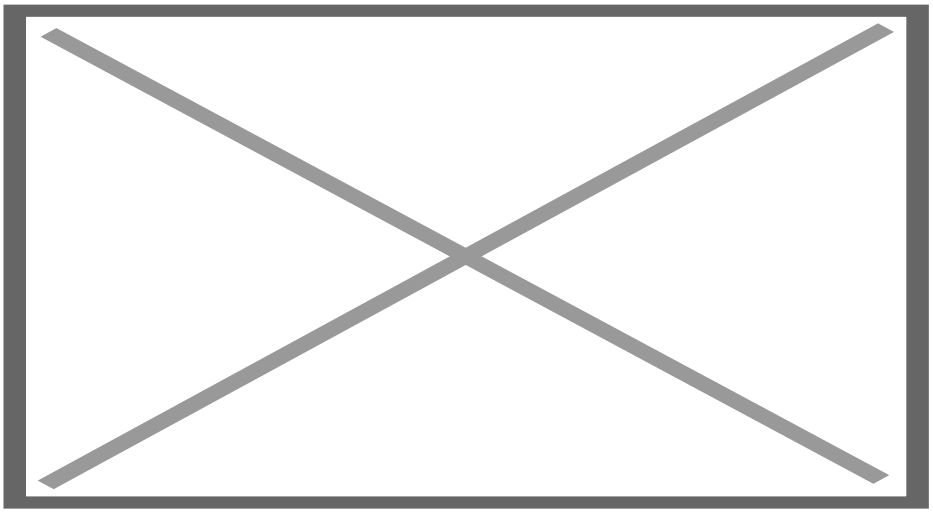
With a focus on innovation and technological development, the University technology degree (BUT) in Electrical Engineering and Industrial Computing is a three-year programme that trains students for the world of tomorrow. It allows students to develop competencies to work in the sectors of the city and industry of the future, smart and connected networks, transport and electromobility, aeronautics, renewable energies, healthcare, audiovisual, robotics, space, and more.

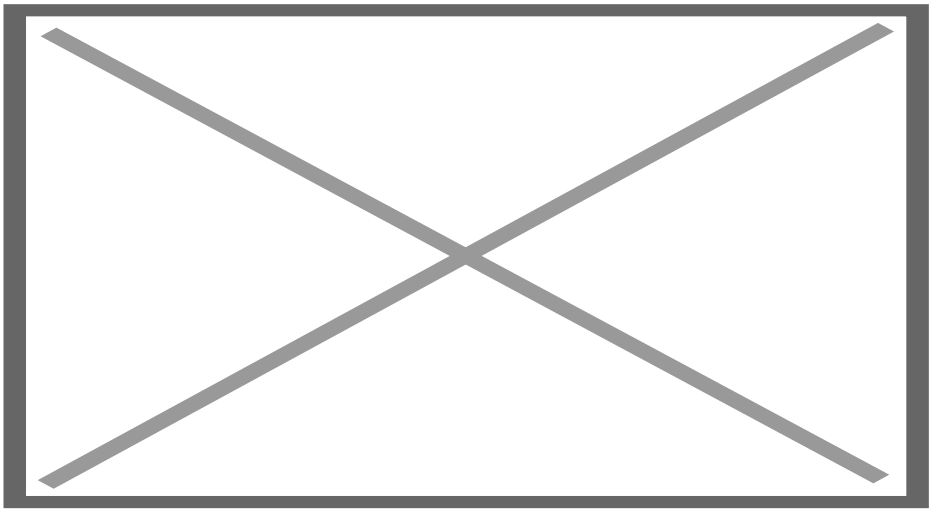
- A curriculum with an excellent support framework: 2000h of courses over 3 years. - A technical curriculum with a strong focus on practice and projects: + 600h of supervised projects over 3 years; + practical work in groups of 12/13 students; + supervised work in groups of 24/26 students; + 22 to 26 weeks of internship, in France or abroad. + Work-study programme available from the 1st year. + Continuous, competencies-based assessment. + Always possible to graduate at Bac+2 with a university technology diploma (DUT). The University technical institute (IUT) at Cachan offers three university technology diploma (BUT) courses in Electrical Engineering and Industrial Computing : - Electricity and Energy Management (EME)) , - Automation & Industrial IT (AII) - Electronics and Embedded Systems (ESE) The choice of course is made at the end of the 1st year.

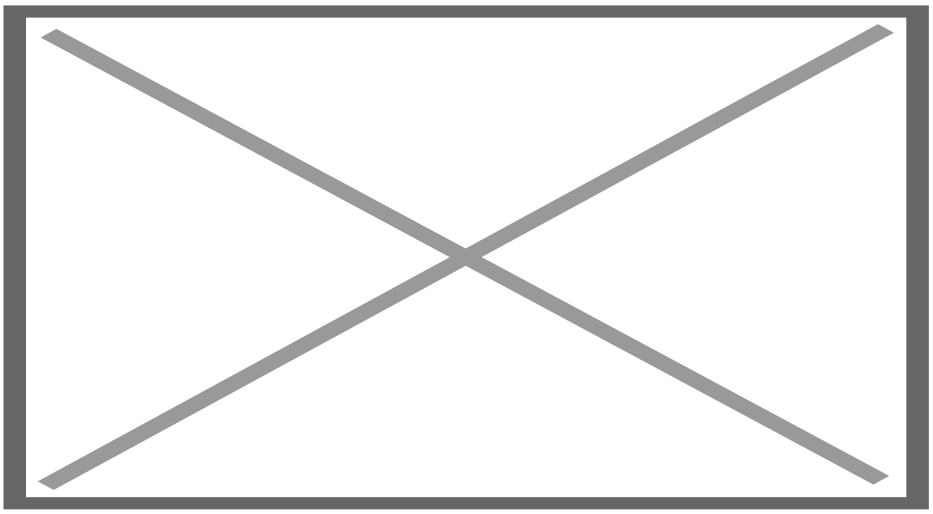
Télécharger la fiche détaillée
Apply Now











Information

- Overview
- Application requirements
- Schedule

Présentation

Skills

Students completing the university technology diploma in Electrical Engineering and Industrial Computing (BUT GEII) will be able to design and implement systems for processing and sending information, and managing and exploiting energy. + Strong scientific and technical culture. + Ability to communicate through various media. + Fluency in technical English. + Knowledge of the professional world. + Autonomy, initiative, team spirit.

Objectives

The Department of Electrical Engineering and Industrial Computing (GEII) offers a curriculum geared towards careers in electricity and electronics (electronics, computing, networks and electricity). The aim is to prepare students in 3 years for senior technician roles, while offering the possibility of further study, particularly through work-study programmes.

Débouchés

Further Study Opportunities

Master of Education and Teaching Professions
Engineering school
Engineering, research and development
Master’s Degree in Engineering or Mechanical Engineering

Admission

Capacity

Available Places

200

Target Audience and Entry Requirements

The BUT is a 3-year university technology diploma that forms part of the European LMD system. A selective field of study, it is open to secondary-school students with scientific or technological baccalaureate degrees, and to students re-orienting their studies (L1, CPGE (Preparatory Classes for the Grandes Écoles), STS (Higher Technician Sections), university technology diploma (DUT), PACES (First Year of the Joint Health Studies Programme), etc.). The BUT is also open to continuing education and RPL students.

Programme

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	distar learn cour
Concevoir la partie GEii d'un système (S3)	7.5	Semestre 1							

Charger le détail

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	distar learn cour
Concevoir la partie GEii d'un système (S4)	7.5	Semestre 2							

Charger le détail

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	distanc learni cours
Vérifier la partie GEII d’un système (S3)	7.5	Semestre 1							

Charger le détail

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	di le
Vérifier la partie GEII d’un système (S4)	7.5	Semestre 2							

Charger le détail

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	distan learni cour
Assurer le maintien en condition opérationnelle d'un système GEii (S3)	7.5	Semestre 1							

Charger le détail

Subjects	ECTS	Semester	Lecture	directed study	practical class	Lecture/directed study	Lecture/practical class	directed study/practical class	distan learni cour
Assurer le maintien en condition opérationnelle d'un système GEii (S4)	7.5	Semestre 2							

Charger le détail

Implanter un système matériel ou logiciel (S3)	7.5	Semestre 1

Charger le détail

Implanter un système matériel ou logiciel (S4)	7.5	Semestre 2

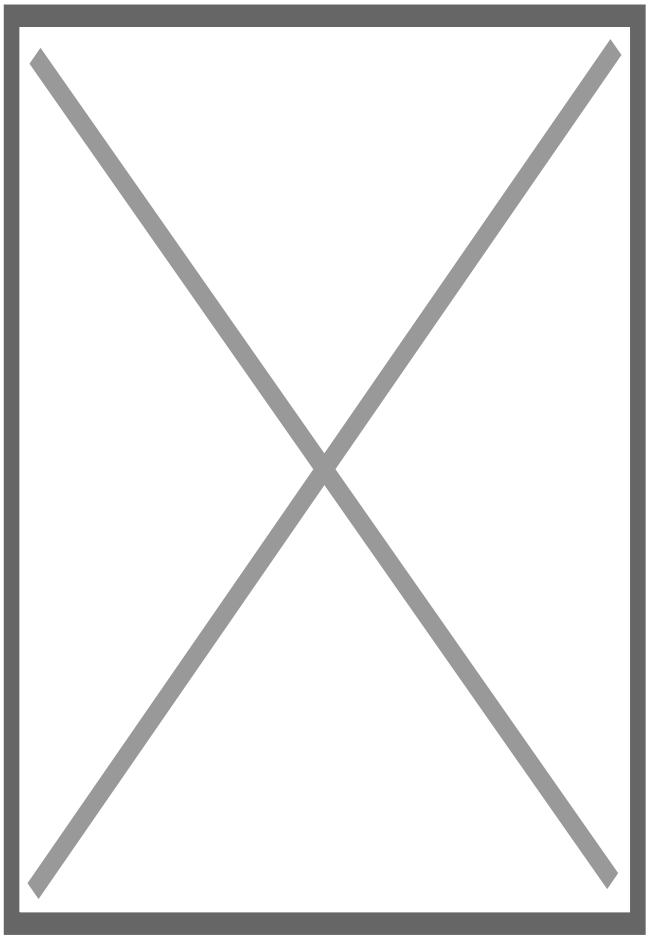
Charger le détail

Teaching Location(s)

CACHAN

Training campus

Javascript est requis pour visualiser cette carte.



Cachan

Cachan
Bus 184, 187
RER B Arcueil-Cachan
Voir
Student restaurant (CROUS)
Student residence
Sports facilities

Contact

- **Xavier MININGER**
- **Jacques-Olivier KLEIN**

http://actu.universite-paris-saclay.fr/formation/genie-electrique-et-informatique-industrielle/but2-genie-electrique-et-informatique-industrielle-electronique-et-systemes-embarques-ese